Electronic Acknowledgement Receipt				
EFS ID:	1404229			
Application Number:	10663144			
International Application Number:				
Confirmation Number:	2156			
Title of Invention:	Method of operating a memory at high speed using a cycle ready state output signal			
First Named Inventor/Applicant Name:	Bryan D. Sheffield			
Customer Number:	23494			
Filer:	Alan K. Stewart/Minerva Aponte			
Filer Authorized By:	Alan K. Stewart			
Attorney Docket Number:	TI-35286			
Receipt Date:	28-DEC-2006			
Filing Date:	16-SEP-2003			
Time Stamp:	14:31:22			
Application Type:	Utility			

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Amendment After Final	TI-35286-amd116.pdf	59230	no	3
Warnings:					

Information:	
Total Files Size (in bytes):	59230

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.